



LOGIC
TECHNOLOGIES

PRODUCT SPECIFICATION

DESCRIPTION

TFT Module – 2.3” a-Si TFT
320 x (RGB) x 240 QVGA

PART NUMBER

LTTD320240023-L1

VERSION

1.2

ROHS COMPLIANT

Revision Status

Revision	Revision Date	Page	Content	Notes
1.0	16.01.2012		Initial release	
1.1	08.11.2013		1. Change the driver IC to ILI9342C 2. Correct the viewing direction to 6:00 and viewing angle	
1.2	01.12.2013		Initial interface is set to 8-bit parallel.	

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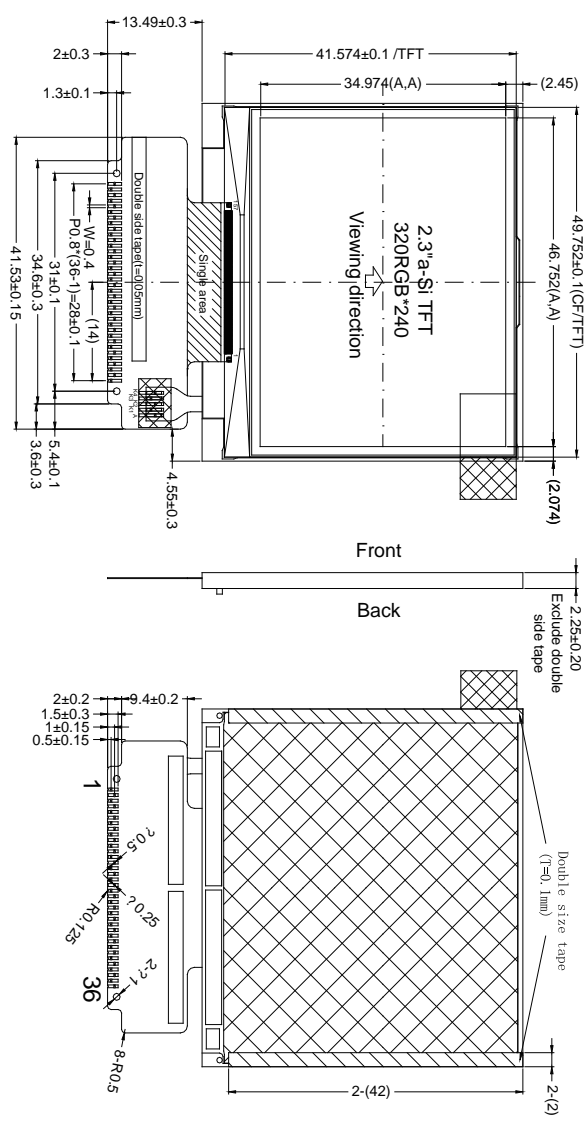
- GENERAL INFORMATION
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- ABSOLUTE MAXIMUM RATINGS
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- RELIABILITY TESTING
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- GENERAL INFORMATION

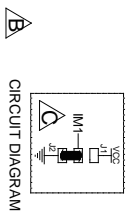
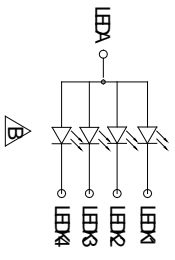
Item	Contents	Unit
LCD Type	TFT Transmissive, normally white, QVGA	---
Technology	a-Si TFT	---
Viewing Direction	6:00	O'clock
Viewing Angle (Gray scale inversion direction)	12:00	O'clock
Module dimensions (W x H x T)	50.90x45.80x2.25	mm
Active area (W x H)	46.752x35.064	mm
Number of pixels	320 (RGB) x 240	---
Pixel pitch	0.1461x 0.1461	mm
Colours	65K/262K	---
Backlight	LED (4 LED parallel)	---
Interface	MCU 8/16-bit parallel	---
TFT panel Controller IC	ILI9342C	---
Operating temperature	-20 to +70	°C
Storage temperature	-30 to +80	°C

MECHANICAL DIMENSIONS

Mechanical Dimensions



- Notes:
1. 2.3" a-Si Normal White TFT
 2. Operating Temperature: -20degC to +70degC
 3. Storage Temperature: -30degC to +80degC
 4. Viewing Direction: 6 O'clock
 5. Gray scale Inversion direction: 12 O'clock
 6. Interface: Initial 8bits MCU interface
 7. Backlight: White /3.2V(Typical)/60mA
 8. General Tolerance: +/-0.3mm
 9. RoHS Compliant



IM3	IM2	IM1	IM0	Interface	Pin in use
0	1	1	0	80 MCU 16-bit bus interface	DB0 - DB15
0	1	0	0	80 MCU 8-bit bus interface	DB0 - DB7

VerA: First release
 VerB: Modify Detail and Model number
 VerC: Initial set Interface to 8-bit MCU

PIN FUNCTION

PIN	SYMBOL	FUNCTION
1	LEDA	LED1
2	LEDK1	LED2
3	LEDK2	LED3
4	LEDK3	LED4
5	LEDK4	
6	GND	
7	RESET	
8	DB15	
9	DB14	
10	DB13	
11	DB12	
12	DB11	
13	DB10	
14	DB9	
15	DB8	
16	DB7	
17	DB6	
18	DB5	
19	DB4	
20	DB3	
21	DB2	
22	DB1	
23	DB0	
24	NC	
25	RD	
26	WR	
27	RS	
28	CS	
29	IOCC1 (42.8V)	
30	VCC(2.8V)	
31	VCC(2.8V)	
32	GND	
33	NC	
34	NC	
35	NC	
36	NC	

Viewing after bending,
 shipment with unfold

LOGIC TECHNOLOGIES

Part Number: LTTD320240023-L1

Revision: C

Not to scale. Unit: mm.

Sheet: 1/1

- ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V_{CC}	- 0.3	3.6	V	GND = 0
Input voltage	V_{IN}	- 0.3	$V_{DD} + 0.3$	V	---
LED Forward Voltage	V_F	-0.3	3.5	V	1 LED ($I_F=20mA$)
LED Forward Current	I_F	0	25	mA	Per LED
Operating Temperature	T_{OPR}	- 20	70	°C	Note 1
Storage temperature	T_{ST}	- 30	80	°C	Note 1

Note 1: Response time will be slower (delayed) at lower temperatures approaching the maximum operating temperature. At temperatures approaching the maximum operating temperature the LCD will experience a darker color. However this phenomena is an inherent property of LCDs and in no way means the LCD has malfunctioned.

- ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Min	Typ.	Max	Unit	Remark
Operating Voltage	V_{CC}	2.6	2.8	3.3	V	
Input high voltage	V_{IH}	$0.8 V_{CC}$	---	V_{CC}	V	
Input low voltage	V_{IL}	- 0.3	---	$0.2 V_{CC}$	V	

- BACKLIGHT CHARACTERISTICS

Item	Symbol	Min.	Typ.	Max.	Unit	Condition
Forward Current Voltage	V_F	2.9	3.2	3.5	V	
Forward Current	I_F	---	60	80	mA	
Backlight Power Consumption	P_{BL}	---	192	---	mW	

Notes

- Backlight drive conditions : external constant current driving method.**

- The LED driving condition is defined for the total backlight consumption.
- Forward Voltage adjusting should depend on Forward Current setting.
- Optical performance should be evaluated at $T_a=25^\circ\text{C}$ only.
- If the LEDs are driven by high current, high ambient temperature & humidity condition the lifetime of the LEDs will be reduced.
- Operating life means brightness reduces to 50% of initial brightness.
- Typical operating lifetime is estimated data.

• ELECTRO-OPTICAL CHARACTERISTICS

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Refer	Note	
Response Time	T _{ON}	Ta=25°C	---	10	15	ms	Fig 1	1	
	T _{OFF}		---	15	25				
Contrast ratio	Cr		400	500	---	---	Fig 2	1	
Uniformity	U	---	70	80	---	%	Fig 2	3	
Surface Luminance	Lv		180	210	---	cd/m ²	Fig 2	2	
Viewing angle ratio		∅=T	60	70	---	deg	Fig 3	6	
		∅=B	45	55	---				
		∅=L	60	70	---				
		∅=R	60	70	---				
CIE (x,y) chromaticity	Red	x	Backlight On	---	0.628	---	---	Fig 2.	5
		y		---	0.327	---			
	Green	x		---	0.287	---			
		y		---	0.562	---			
	Blue	x		---	0.136	---			
		y		---	0.126	---			
	White	x		---	0.303	---			
		y		---	0.332	---			

Note

1. Contrast ratio (CR) is defined mathematically in Figure 2.

$$\text{Contrast Ratio} = \frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)}}$$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see figure 2.

$$Lv = \text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5...)}$$

Note 3. Uniformity of surface luminance, White, is defined mathematically in figure 2.

$$\text{White} = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}$$

Note 4. Response time is the time required for the display to transition from white to black (rise time Tr) and from black to white (decay or fall time, Tf). The industry standard test equipment used is the Autronic-Melcher's Conoscope.

Note 5. CIE (x,y) chromaticity. The x,y value is determined by measuring luminance at each test position 1 through 5, then calculating the average value.

Note 6. The Viewing angle is the angle at which the contrast ratio is greater than 2. For a TFT module, the contrast ratio is greater than 10. The angles are determined for the horizontal or 'x' axis and the vertical or 'y' axis with respect to the 'z' axis, being the LCD surface reference. Also see figure 3.

Note 7. For viewing angle and response time testing, the testing data is based on Autronic-Melcher's BM-7A. For the contrast ratio, surface luminance, luminance uniformity and chromaticity (CIE), the test data is based on the industry's standard SR-3A photo detector.

Note 8. For TFT modules, grey scale reversing occurs in the direction of the panel viewing angle.

Figure 1. Definition of response time

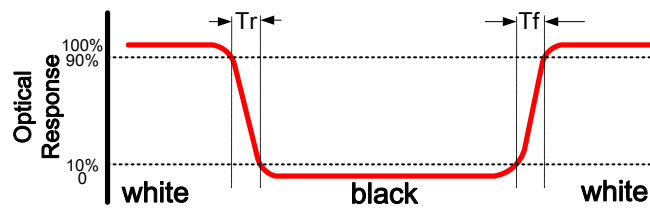


Figure 2. Measuring contrast ratio, surface luminance, luminance uniformity and CIE (chromaticity.)

A : 5mm, B : 5mm, H, V : Active area, Light spot size
Measurement instrument is Topcon's luminance meter BM-5.

mm, 500mm distance from the LCD surface to the detector lens.

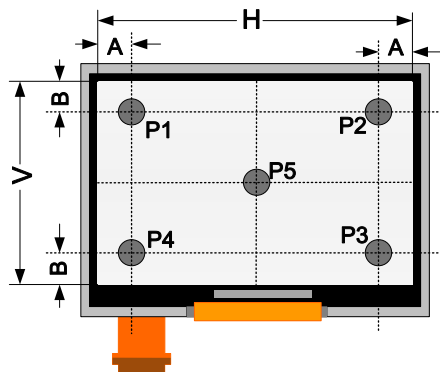
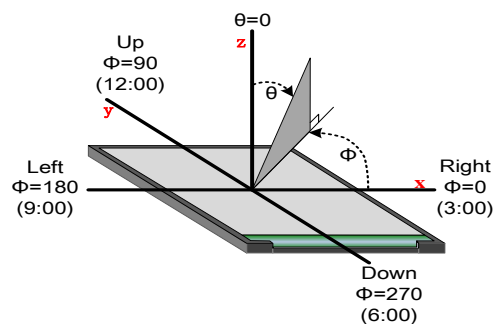


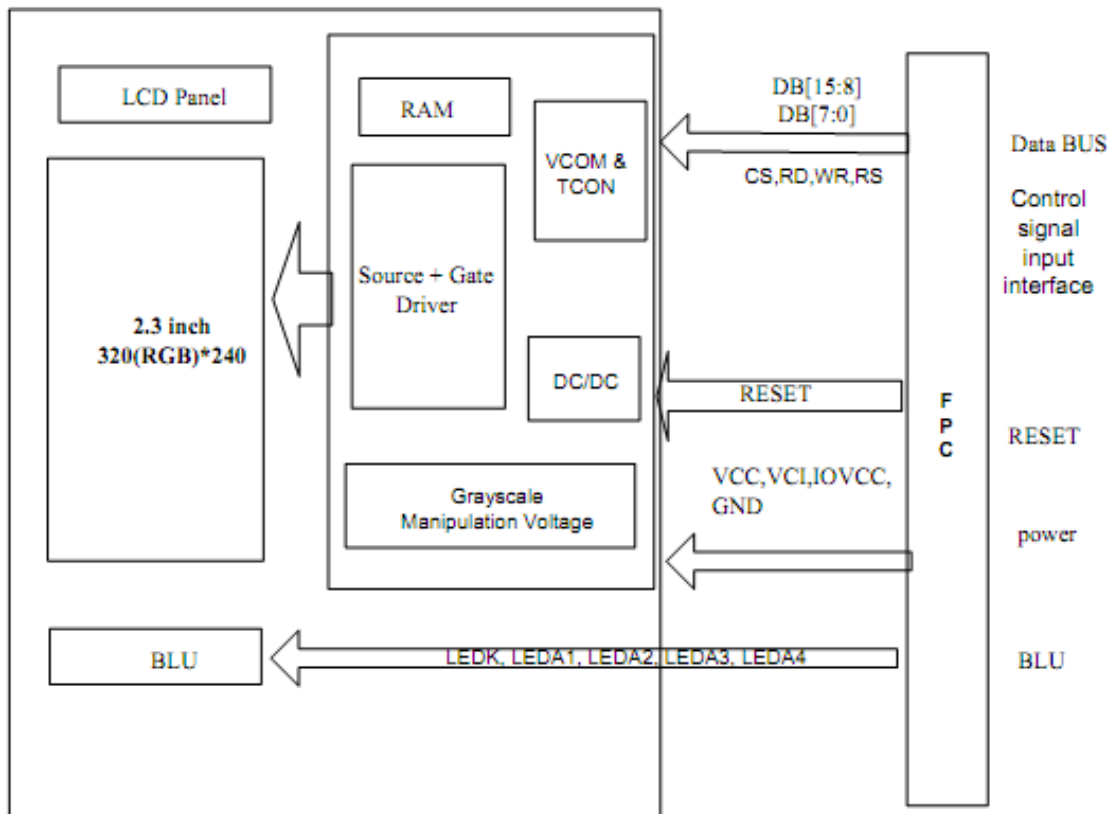
Figure 3. Definition of viewing angle



- INTERFACE DESCRIPTION

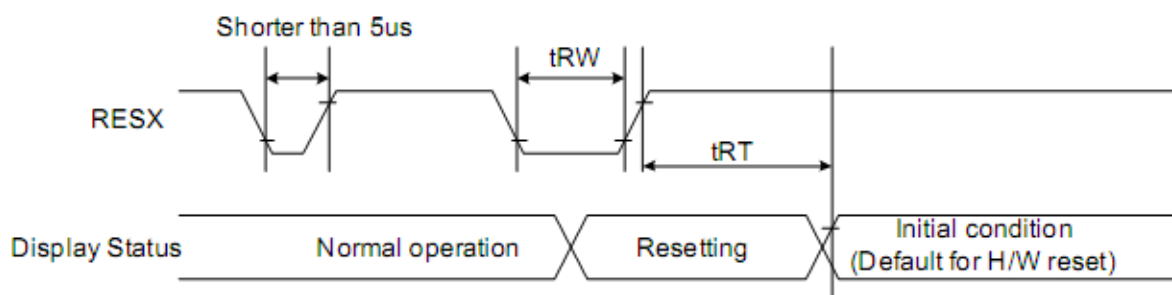
No.	Symbol	Function Remark
1	LEDA	LED Anode
2	LEDK1	LED Cathode
3	LEDK2	LED Cathode
4	LEDK3	LED Cathode
5	LEDK4	LED Cathode
6	GND	Power Ground
7	RESET	Input RESET signal
8	DB15	Data Bus
9	DB14	Data Bus
10	DB13	Data Bus
11	DB12	Data Bus
12	DB11	Data Bus
13	DB10	Data Bus
14	DB9	Data Bus
15	DB8	Data Bus
16	DB7	Data Bus
17	DB6	Data Bus
18	DB5	Data Bus
19	DB4	Data Bus
20	DB3	Data Bus
21	DB2	Data Bus
22	DB1	Data Bus
23	DB0	Data Bus
24	NC	Not Connected
25	RD	Read strobe signal and enables an operation to read out data when the signal is low.
26	WR	Write strobe signal and enables an operation to write data when the signal is low.
27	RS	Register select signal
28	CS	Chip select signal
29	IOVCC(1.8/2.8V)	IO Pad and Digital power supply
30	VCC(2.8V)	Analog power supply
31	VCC(2.8V)	Analog power supply
32	GND	Power Ground
33	NC	Not Connected
34	NC	Not Connected
35	NC	Not Connected
36	NC	Not Connected

• Block Diagram



• TIMING DIAGRAM

Reset Timing



Signal	Symbol	Parameter	Min	Max	Unit
RESX	tRW	Reset pulse duration	10		uS
	tRT	Reset cancel		5 (note 1,5)	mS
				120 (note 1,6,7)	mS

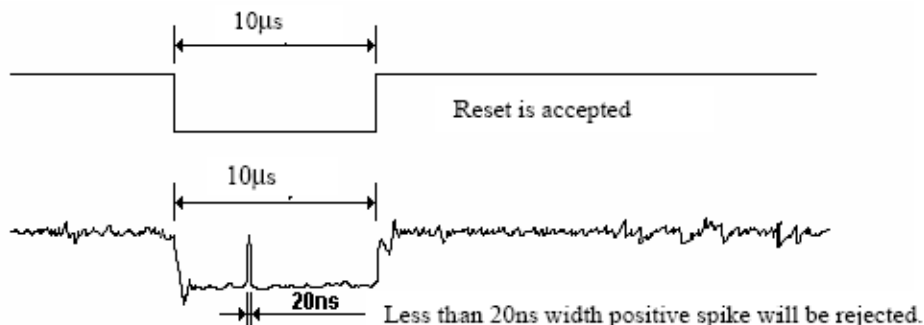
Note 1: The reset cancel includes also required time for loading ID bytes, VCOM setting and other settings from NV memory to registers. This loading is done every time when there is HW reset cancel time (tRT) within 5 ms after a rising edge of RESX.

Note 2: Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below: -

RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 10us	Reset
Between 5us and 10us	Reset starts

Note 3: During the Resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out –mode. The display remains the blank state in Sleep In -mode.) And then return to Default condition for Hardware Reset.

Note 4: Spike Rejection also applies during a valid reset pulse as shown below:

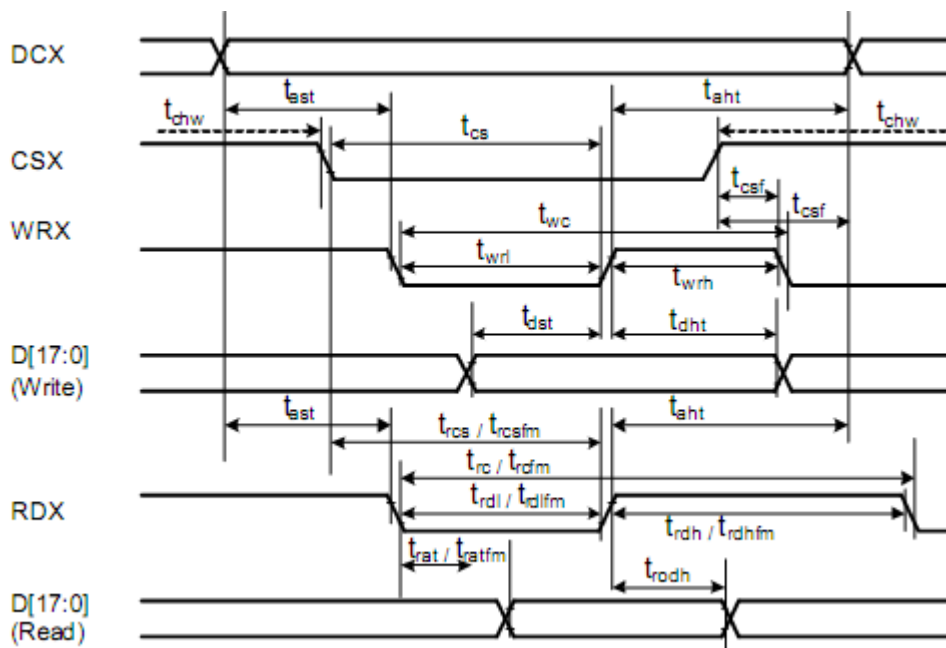


Note 5: When Reset applied during Sleep In Mode.

Note 6: When Reset applied during Sleep Out Mode.

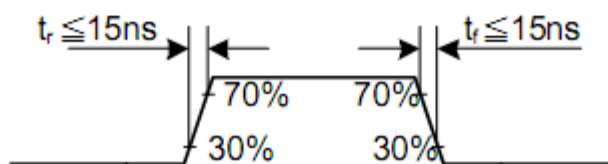
Note 7: It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec.

Parallel 8/16 bit timing characteristics (8080-system)



Signal	Symbol	Parameter	min	max	Unit	Description
DCX	t_{est}	Address setup time	0	-	ns	
	t_{ah}	Address hold time (Write/Read)	10	-	ns	
CSX	t_{chw}	CSX "H" pulse width	0	-	ns	
	t_{cs}	Chip Select setup time (Write)	15	-	ns	
	t_{rcs}	Chip Select setup time (Read ID)	45	-	ns	
	t_{rcsfm}	Chip Select setup time (Read FM)	355	-	ns	
	t_{csf}	Chip Select Wait time (Write/Read)	10	-	ns	
WRX	t_{wc}	Write cycle	66	-	ns	
	t_{wrh}	Write Control pulse H duration	33	-	ns	
	t_{wrl}	Write Control pulse L duration	33	-	ns	
RDX (ID)	t_{rc}	Read cycle (ID)	160	-	ns	When read ID data
	t_{rdh}	Read Control pulse H duration	90	-	ns	
	t_{rdl}	Read Control pulse L duration	45	-	ns	
RDX (FM)	t_{rdlfm}	Read Cycle (FM)	450	-	ns	When read from the frame memory
	t_{rdhfm}	Read Control H duration (FM)	90	-	ns	
	t_{rdlfm}	Read Control L duration (FM)	355	-	ns	
DB[17:0], DB[15:0], DB[8:0], DB[7:0]	t_{dst}	Write data setup time	10	-	ns	For maximum CL=30pF For minimum CL=8pF
	t_{dht}	Write data hold time	10	-	ns	
	t_{rat}	Read access time	-	40	ns	
	t_{ratfm}	Read access time	-	340	ns	
	t_{rod}	Read output disable time	20	80	ns	

Note: $T_a = -40$ to 85 °C, $IOVCC=1.65V$ to $3.3V$, $VCI=2.3V$ to $4.8V$, $DGND=0V$



Note: Details please refer to ILI9342 specification

- RELIABILITY TESTING

NO.	Item	Condition	Criteria
1	High Temperature Operating	70°C +/-2°C, 160Hrs	IEC60068-2-1, GB2423.2
2	Low Temperature Operating	-20°C +/-2°C, 160Hrs	IEC60068-2-1 GB2423.1
3	High Temperature Storage	80°C +/-2°C, 240Hrs	IEC60068-2-1 GB2423.2
4	Low Temperature Storage	-30°C +/-2°C, 240Hrs	IEC60068-2-1 GB2423.2
5	Hi Temperature & High Humidity Operation	60°C, 90%RH max,96Hrs	IEC60068-2-78 GB/T2423.3
6	Vibration (non operating)	Frequency range:10~55Hz, Stroke:1.5mm Sweep:10Hz~ 55Hz~10Hz 2hours for each direction of x.y.z (6 hours for total)	IEC60068-2-6 GB/T2423.10
7	Package Vibration Test	Random Vibration: 0.015GxG/Hz for 5-200Hz, -6dB/Octave from 200-500Hz 2 hours for each direction of X,Y,Z (6 hours for total)	IEC60068-2-34 GB/T2423.11
8	Thermal Shock (non operating)	-20°C to 30min to 70°C, 30min Change time: 5min, 10 cycles	Start with cold temperature, End with high temperature, IEC60068-2- 14:1984,GB2423.22
9	Drop Test (packaged)	Height:80 cm,1 corner, 3 edges, 6 surfaces	IEC60068-2-32 GB/T2423.8
10	Shock (non-operation)	80G 6ms, ±X,±Y,±Z 3 times for each direction	IEC60068-2-27 GB/T2423.5
11	ESD (operation)	C=150pF,R=330Ω, Air:±8Kv, Contact:±4Kv, 10times/terminal	IEC61000-4-2 GB/T17626.2

Notes:

1. Test samples are applied to one test item.
 2. Sample size for each test item is 2-10pcs.
 3. For humidity testing, a pure water resistance of $>10\text{MW}$ should be used.
 4. (a) In the case of a malfunction caused by ESD damage, if the LCM returns to its normal state after resetting, the item is considered to have passed the ESD test.
 (b) It is recommended to use an anti-static blower (ioniser) to reduce the electro-static voltage in the working area.
 (c) When removing the protection film from the LCM panel, peel off the film slowly (more than 1sec) while blowing the ioniser towards the peeling area to minimize ESD. This will reduce the risk of damaging the electrical circuitry.
 5. EL backlights are exempt from the reliability testing with respect to temperature and humidity. Some defects may occur such as black spots or blemishes due to the inherent chemical reaction of humidity with fluorescent EL.
 6. If it is installed, please use the automatic test mode on the LCM &/or demonstration box when testing.
- INSPECTION CRITERIA

This specification is designed to be used as the standard acceptance/rejection criteria for normal LCM products.

1. Sampling plan.

The sampling plan according to GB/T 2828.1-2003 / ISO2859-16 1999 and ANSI/ASQC Z1.4 1993, normal level 2 and based on:

- Major defect: AQL 0.65
- Minor defect: AQL 1.5

2. Inspection condition

- The viewing distance for cosmetic inspection is approximately 30cm with the naked eye, and under an environment of 20-40W light intensity, in all directions, within 45° against a perpendicular line. (Normal temperature $20-25^\circ\text{C}$ and normal humidity $60\pm 15\text{ RH}$.)
- Driving voltage - The Vop value from which the most optimal contrast can be obtained near the specified Vop in the specification (within $\pm 0.5\text{V}$ of the typical value at 25°C .)

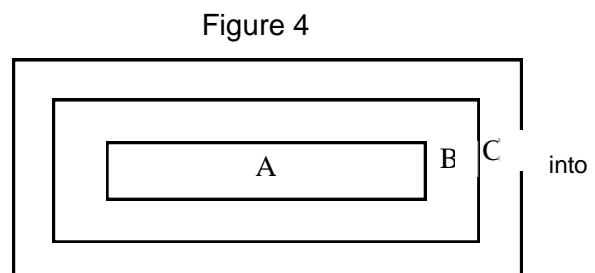
3. Definition of inspection zone in LCD.

Zone A : active pixel area

Zone B : viewing area except Zone A (Zone A + Zone B = Minimum viewing area)

Zone C : Outside viewing area (invisible area after assembling customer's product.

Figure 4 inspection zones in an LCD



Note: As a general rule visual defects in Zone C are permissible when there is no visual effect once assembled into the customer's product.

• INSPECTION STANDARD

○ MAJOR CRITERIA

Item No	Item to be inspected	Inspection standard	Classification
1	All functional defects	1) No display 2) Display abnormal 3) Missing vertical or horizontal segment 4) Short circuit 5) Backlight not working, flickering and abnormal light	Major
2	Missing	Missing component	
3	Outline dimension	Overall outline dimension beyond the drawing dimension is not allowed	

○ COSMETIC CRITERIA

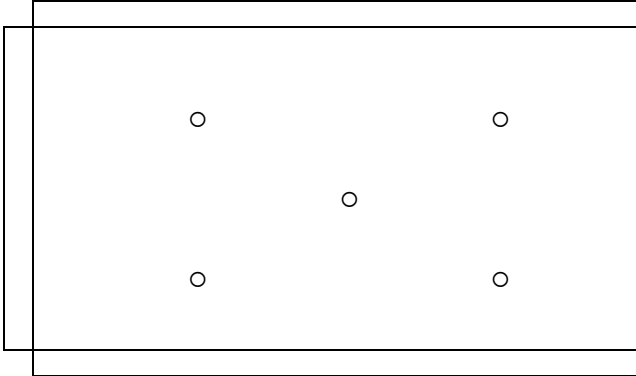
No.	Item	Judgment Criteria	Partition
1	Difference in Spec.	None allowed	Major
2	Pattern peeling	No substrate pattern peeling and floating	Major
3	Soldering defects	No soldering missing No soldering bridge No cold soldering	Major Major Minor
4	Resist flaw on substrate	Invisible copper foil (∅0.5mm or more) on substrate pattern	Minor
5	Accretion of metallic Foreign matter	No soldering dust No accretion of metallic foreign matters (Not exceed ∅0.2mm)	Minor Minor
6	Stain	No stain to spoil cosmetic badly	Minor
7	Plate discoloring	No plate fading, rusting and discoloring	Minor
8	Solder amount 1. Lead parts	a. Soldering side of PCB Solder to form a 'Filet' all around the lead. Solder should not hide the lead form perfectly. (too much) b. Components side (In case of 'Through Hole PCB') Solder to reach the Components side of PCB.	Minor
	2. Flat packages	Either 'toe' (A) or 'heel' (B) of the lead to be covered by 'Filet'. Lead form to be assume over solder.	
	3. Chips	$(3/2) H \geq h \geq (1/2) H$	
9	Solder ball/solder splash	a) The spacing between solder ball and the conductor or solder pad $h \geq 0.13\text{mm}$. The diameter of the solder ball $d \leq 0.15\text{mm}$. b) The quantity of solder balls or solder splashes isn't more than 5 in 600mm ² . c) Solder balls / splashes do not violate minimum electrical clearance d) Solder balls/splashes must be not be able to be dislodged with normal product usage	Minor Minor Major Minor

○ COSMETIC CRITERIA (non-operating)

No.	Defect	Judgment Criteria	Classification										
1	Spots	In accordance with Screen Cosmetic Criteria (Operating) No.1.	Minor										
2	Lines	In accordance with Screen Cosmetic Criteria (Operating) No.2.	Minor										
3	Bubbles in polarizer	<table border="1"> <thead> <tr> <th>Size : d mm</th> <th>Acceptable Qty in active area</th> </tr> </thead> <tbody> <tr> <td>d ≤ 0.3</td> <td>Disregard</td> </tr> <tr> <td>0.3 < d ≤ 1.0</td> <td>3</td> </tr> <tr> <td>1.0 < d ≤ 1.5</td> <td>1</td> </tr> <tr> <td>1.5 < d</td> <td>0</td> </tr> </tbody> </table>	Size : d mm	Acceptable Qty in active area	d ≤ 0.3	Disregard	0.3 < d ≤ 1.0	3	1.0 < d ≤ 1.5	1	1.5 < d	0	Minor
Size : d mm	Acceptable Qty in active area												
d ≤ 0.3	Disregard												
0.3 < d ≤ 1.0	3												
1.0 < d ≤ 1.5	1												
1.5 < d	0												
4	Scratch	In accordance with spots and lines operating cosmetic criteria. When the light reflects on the panel surface, the scratches are not to be remarkable.	Minor										
5	Allowable density	Above defects should be separated more than 30mm each other.	Minor										
6	Coloration	Not to be noticeable coloration in the viewing area of the LCD panels. Back-lit type should be judged with back-lit on state only.	Minor										
7	Contamination	Not to be noticeable.	Minor										

○ COSMETIC CRITERIA (operating)

No.	Defect	Judgment Criteria	Classification																				
1	Spots	<p>A) Clear</p> <table border="1"> <thead> <tr> <th>Size : d mm</th> <th>Acceptable Qty in active area</th> </tr> </thead> <tbody> <tr> <td>d ≤ 0.1</td> <td>Disregard</td> </tr> <tr> <td>0.1 < d ≤ 0.2</td> <td>6</td> </tr> <tr> <td>0.2 < d ≤ 0.3</td> <td>2</td> </tr> <tr> <td>0.3 < d</td> <td>0</td> </tr> </tbody> </table> <p>Note : Including pin holes and defective dots which must be within one pixel size.</p> <p>B) Unclear</p> <table border="1"> <thead> <tr> <th>Size : d mm</th> <th>Acceptable Qty in active area</th> </tr> </thead> <tbody> <tr> <td>d ≤ 0.2</td> <td>Disregard</td> </tr> <tr> <td>0.2 < d ≤ 0.5</td> <td>6</td> </tr> <tr> <td>0.5 < d ≤ 0.7</td> <td>2</td> </tr> <tr> <td>0.7 < d</td> <td>0</td> </tr> </tbody> </table>	Size : d mm	Acceptable Qty in active area	d ≤ 0.1	Disregard	0.1 < d ≤ 0.2	6	0.2 < d ≤ 0.3	2	0.3 < d	0	Size : d mm	Acceptable Qty in active area	d ≤ 0.2	Disregard	0.2 < d ≤ 0.5	6	0.5 < d ≤ 0.7	2	0.7 < d	0	Minor
Size : d mm	Acceptable Qty in active area																						
d ≤ 0.1	Disregard																						
0.1 < d ≤ 0.2	6																						
0.2 < d ≤ 0.3	2																						
0.3 < d	0																						
Size : d mm	Acceptable Qty in active area																						
d ≤ 0.2	Disregard																						
0.2 < d ≤ 0.5	6																						
0.5 < d ≤ 0.7	2																						
0.7 < d	0																						
2	Lines	<p>A) Clear</p> <p>Note : () - Acceptable Qty in active area L - Length (mm) W - Width (mm) ∞ - Disregard</p> <p>B) Unclear</p>	Minor																				
3	Rubbing line	Not to be noticeable.																					

4	Allowable density	Above defects should be separated more than 10mm each other.	Minor
5	Rainbow	Not to be noticeable.	Minor
6	Dot size	To be 95% ~ 105% of the dot size (Typ.) in drawing. Partial defects of each dot (ex. pin-hole) should be treated as 'spot'. (see Screen Cosmetic Criteria (Operating) No.1)	Minor
7	Uneven brightness (only back-lit type module)	<p>Uneven brightness must be $B_{MAX} / B_{MIN} \leq 2$</p> <p>- B_{MAX} : Max. value by measure in 5 points - B_{MIN} : Min. value by measure in 5 points</p> <p>Divide active area into 4 vertically and horizontally. Measure 5 points shown in the following figure.</p>  <p>○ : Measuring points</p>	Minor

Note :

- (1) Size : $d = (\text{long length} + \text{short length}) / 2$
- (2) The limit samples for each item have priority.
- (3) Complex defects are defined item by item, but if the number of defects are defined in above table, the total number should not exceed 10.
- (4) In case of 'concentration', even the spots or the lines of 'disregarded' size should not allowed.

Following three situations should be treated as 'concentration'.

- 7 or over defects in circle of $\varnothing 5\text{mm}$.
- 10 or over defects in circle of $\varnothing 10\text{mm}$.
- 20 or over defects in circle of $\varnothing 20\text{mm}$.

• PRECAUTIONS FOR USING LCD MODULES

○ Handling Precautions

(1) The display panel is made of glass. Do not subject it to a mechanical shock by dropping it or impact.

(2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.

(3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

(4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

(5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents :

- Isopropyl alcohol
- Ethyl alcohol

(6) Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.

- Water
- Ketone

- Aromatic solvents

(7) Exercise care to minimize corrosion of the electrode. Water droplets, moisture condensation or a current flow in a high-humidity environment, accelerate corrosion of the electrodes.

(8) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.

(9) Do not attempt to disassemble or process the LCD module.

(10) NC terminal should be open. Do not connect anything.

(11) If the logic circuit power is off, do not apply the input signals.

(12) To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

- Be sure to ground the body when handling the LCD modules.

- Tools required for assembling, such as soldering irons, must be properly grounded.

- To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions.

- The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

- o Storage Precautions

When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps. Keep the modules in bags (avoid high temperature / high humidity and low temperatures below the stated storage temperature of the LCM specification).

- o Others

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.

If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.

- Exposed area of the printed circuit board.

- Terminal electrode sections.

- USING LCD MODULES

- o Liquid Crystal Display Modules

LCD is composed of glass and polarizer. Pay attention to the following items when handling.

- (1) Please keep the temperature within specified range for use and storage. Polariser degradation, bubble generation or polariser peel-off may occur if exposed to high temperature and high humidity.
- (2) Do not touch, push or rub the exposed polarisers with anything harder than an HB pencil lead (no glass object, or metal objects such as pins, tweezers, etc,)
- (3) N-hexane is recommended for cleaning the adhesives used to attach front/rear polarisers and reflectors made of organic substances that may be damaged by chemicals such as acetone, toluene, ethanol and isopropyl alcohol.
- (4) When the display surface becomes dusty, wipe gently with absorbent cotton or other soft material

like chamois soaked in petroleum benzene. Do not scrub hard to avoid damaging the display surface.

- (5) Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading.
- (6) Avoid contacting oil and fats.
- (7) Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarisers. After products are tested at low temperature they must be warmed up in a container before coming in contact with room temperature air.
- (8) Do not put or attach anything on the display area to avoid leaving marks on.
- (9) Do not touch the display with bare hands. This will stain the display area and degrade the insulation between the terminals.
- (10) As the LCD glass is fragile, take care when handling especially on the edges.
- (11) Mounting Method – take care when handling in areas such as any PCB mounting holes, polarisers and any area on the perimeter of the LCM that can be easily damaged.
- (12) Caution against static charge - the LCD Module uses C-MOS LSI drivers, so we strongly recommend that you connect any unused input terminal to VDD or VSS, and DO NOT input any signal before power is turned on. Ensure to ground your body, any work/assembly table/area, and assembly equipment to protect against static electricity.

- PRECAUTIONS FOR SOLDERING LCD MODULES

	Manual Soldering	Machine Drag Soldering	Machine Pre-soldering
Non ROHS Product	290°C ~ 350°C Speed : 3 ~ 5 mm/s	330°C ~ 350°C Speed : 4 ~ 8mm/s	300°C ~ 330°C Time : 3 ~ 6S Pressure : 0.8 to 1.2Mpa
RoHS Product	340°C ~ 370°C Time : 3 ~ 5S.	350°C ~ 370°C Time : 4 ~ 8 mm/S.	330°C ~ 360°C Time : 3 ~ 6S. Pressure : 0.8 ~ 1.2Mpa.

- (1) If solder flux is used, be sure to remove any remaining flux after finishing the soldering process. (This does not apply in the case of a non-halogen type of flux.) It is recommended that you protect the LCD surface with a cover during the soldering process to prevent any damage due to the flux sparks.
- (2) When soldering a backlight panel and PCB, the panel and PCB should not be detached more than 3 times. The temperature determines this number and time conditions as mentioned in the above table, although there may be some variance depending on the actual temperature of the soldering iron.
- (3) When removing a backlight panel from the PCB, ensure the solder has completely melted, otherwise the solder pads on the backlight panel and/or PCB may be damaged.

- Caution for operation

- (1) It is recommended to drive LCDs within their specified voltage limit since the higher voltage than the upper limit shortens the LCD life. An electrochemical reaction due to direct current causes the LCD to deteriorate. Therefore, avoid the use of direct current drive.
- (2) Response time will be extremely delayed at lower temperatures than the operating temperature range. At higher temperatures LCD's will experience a dark color. However those phenomena do not mean a malfunction or the LCD's. Once the LCDs are returned to the specified operating temperature range, the response time and colouration should return to the normal state.
- (3) If the display area is physically pressed hard during it's operation, some pixels may be abnormally displayed, but should return to their normal condition after resetting the LCM.
- (4) Moisture sitting on the LCM terminals is a cause for an electro-chemical reaction resulting in a
- (5) terminal open circuit. Usage under the relative condition of 40°C, 50%RH or less is therefore required.

- Safety

- (1) It is recommended to crush any damaged or unnecessary LCDs into pieces and wash off the liquid crystal by using solvents such as acetone and ethanol, which should then be burned up later.
- (2) When any liquid crystal has leaked out of a damaged glass cell and comes in contact with skin, please wash it off well with soap and water.

- Warranty

Unless otherwise agreed between Logic Technologies Ltd and the customer, Logic Technologies will replace or repair any of its products that are found to be functionally defective when inspected in accordance with Logic Technologies' acceptance criteria (copies available upon request) for a period of one year from date of shipment. Cosmetic/visual defects must be returned to Logic Technologies within 90 days of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of Logic Technologies is limited to the repair and/or replacement on the terms set forth above. Logic Technologies will not be responsible for any subsequent or consequential losses and/or events.

Returning products under warranty

No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are:

- Broken LCD glass.
- PCB eyelet's damaged or modified.
- PCB conductors damaged.
- Circuit modified in any way, including addition of components.
- PCB tampered with by grinding, engraving or painting varnish.
- Soldering to or modifying the bezel in any manner.

Product repairs will be invoiced to the customer upon mutual agreement. Products must be returned with sufficient description of the failures and/or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB eyelet's, conductors and terminals.